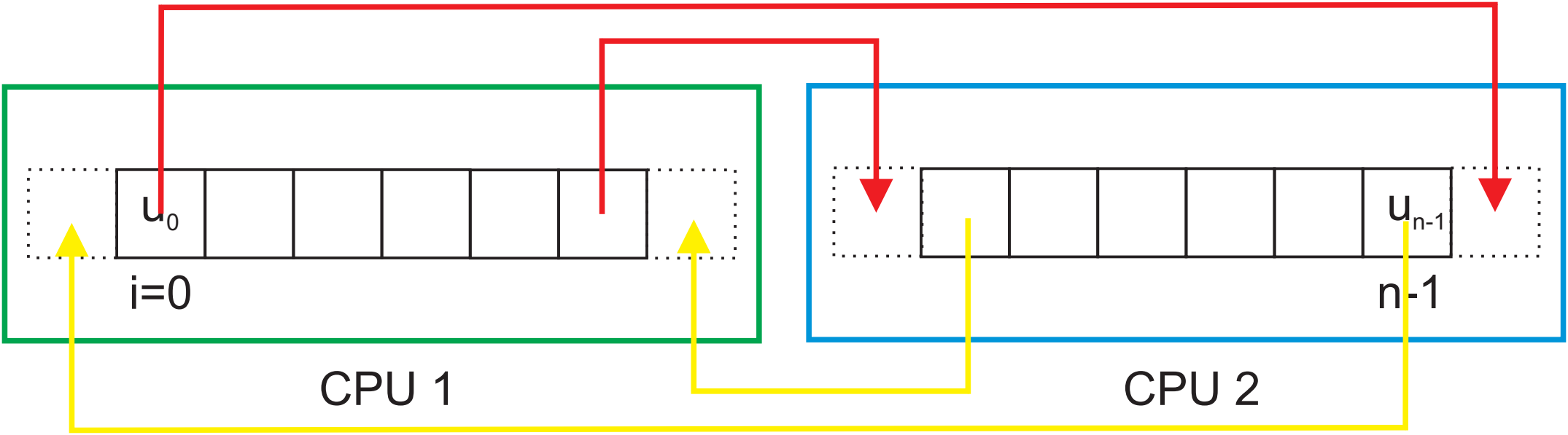

u_0
i=0
u_{n-1}
n-1
CPU 1
CPU 2